

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT Sheet 1 of 1

Docket No. P6167-US-NP-0

Applicant: Shlomo D. Novotny, Arthur S. Rousmaniere and Marlin Vogel  
Serial No: 09/995,362  
Filed: November 27, 2001  
For: METHOD AND SYSTEM FOR COOLING ELECTRONIC COMPONENTS  
Examiner: Ljiljana Ciric  
Art Unit: 3753

## U.S. PATENT DOCUMENTS

Exam Inits	Cite No.	Document Number	Kind Code	Patentee or Applicant Name	Publication Date
AVC		2003209023	A1	Spinazzola	11/13/2003
		4,495,545	B1	Dufresne, et al.	1/22/1985
		6,042,348	A1	Aakalu, et al.	3/28/2000
		6,364,761	B1	Steinbrecher	4/2/2002
		6,397,618	B1	Chu	6/4/2002
✓		2002173267	A1	Sharp	11/21/2002
			A1		

## FOREIGN PATENT DOCUMENTS

Exam Inits	Cite No.	Cy	Number	Kind Code	Patentee or Applicant Name	Publication Date	T
AVC		EP	0 746 192	A	NEC Corporation	12/4/1996	<input type="checkbox"/>
AVC		FR	2 568 172	A	Bull Sems	2/7/1986	<input type="checkbox"/>
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							<input type="checkbox"/>

## OTHER PRIOR ART - NON PATENT LITERATURE AND DOCUMENTS

Exam Inits	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the articles (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
AVC	-	International Searching Authority, "International Search Report from PCT/US2005/003284 (S0104/7121WO)", August 4, 2005, Patent Cooperation Treaty	<input type="checkbox"/>
AVC	-	International Searching Authority, "International Search Report from PCT/US2005/002605 (S0104/7096WO)", August 1, 2005, Patent Cooperation Treaty	<input type="checkbox"/>
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Examiner  
Signature

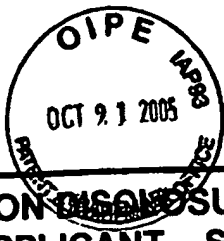
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Date  
Considered

March 17, 2006

Information Disclosure Statement

Paper No. 10212005A



**INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT Sheet 1 of 1**

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**U.S. PATENT DOCUMENTS**

Exam Inits	Cite No.	Document Number	Kind Code	Patentee or Applicant Name	Publication Date
ARC		20020191430	A1	Meir	12/2002
		20030147216	A1	Patel, et al.	8/2003
		20040264124	A1	Patel, et al.	12/2004
		5,115,225	B1	Dao, et al.	5/1992
		5,714,938	B1	Schwabl	2/1998
		6,042,164	B1	Sorbel	2/2000
		6,115,251	B1	Patel, et al.	9/2000
		6,170,561	B1	O'Grady	1/2001
		6,317,321	B1	Fitch	11/2001
		6,421,240	B1	Patel	7/2002
			A1		
			A1		
			A1		
			A1		
			A1		
			A1		
			A1		
			A1		
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Examiner  
Signature

*Ljiljana Ciric*

Date  
Considered

*3/17/2006*

Information Disclosure Statement

*Paper No. 1021 2005 B*